

UNIVERSITY OF CAMBRIDGE INTERNATIONAL EXAMINATIONS General Certificate of Education

Advanced Subsidiary Level and Advanced Level

CANDIDATE
NAME

CENTRE NUMBER

	l	1	1

CANDIDATE NUMBER

9691/12

COMPUTING

Paper 1

October/November 2011 1 hour 30 minutes

Candidates answer on the Question Paper.

No additional materials are required.

No calculators allowed.

READ THESE INSTRUCTIONS FIRST

Write your Centre number, candidate number and name on all the work you hand in.

Write in dark blue or black pen.

You may use a soft pencil for any diagrams, graphs or rough working.

Do not use staples, paper clips, highlighters, glue or correction fluid.

DO NOT WRITE IN ANY BARCODES.

Answer all questions.

No marks will be awarded for using brand names for software packages or hardware.

At the end of the examination, fasten all your work securely together.

The number of marks is given in brackets [] at the end of each question or part question.



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(a)	Sta	te the purpose of the following:
	(i)	Input devices
	(ii)	Output devices.
		[2]
(b)		oint-of-sale (POS) terminal in a supermarket has a number of input devices. te two different input devices used at the POS terminal and state the purpose of th.
	1	
	2	
	•••••	[4]
(c)		umber of point-of-sale terminals are controlled by a multi-user operating system. scribe a multi-user operating system.

2	(a)		he system development life cycle describe what is involved when analysiuirements of a system.
			[4]
	(b)		en a new system is developed it must be installed into the organisation for which it been produced.
		Des	scribe:
		(i)	direct implementation
			[2]
		(ii)	phased implementation
			[2]

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	The state of the s	
	4	
3	An educational game is being produced to help very young children learn about numbers application.	For iner's
		Se.C.
		Th
	[5]	
	[V]	
4	Explain how a knowledge-based (expert) system can be set up and used to identify mineral samples collected by geologists.	
	[6]	

(a)	(i)	Describe the structure of a two-dimensional array.
		[2]
	(ii)	Explain how the contents of a two-dimensional array can be initialised to zero.
		[4]
(b)	Des	scribe how a stack is implemented using an array.
		[4]

6	State ar	n example of each of the following types of storage medium and give a use for
	(i)	Magnetic
		Example
		Use
		[2]
	(ii)	Solid state
		Example
		Use
		[2]

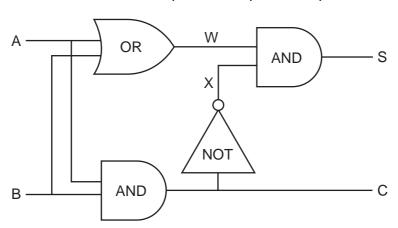
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		7	\
)		ail order company allows customers to make orders by telephoning operator rators type the details of the order at a terminal.	ann
	(i)	7 ail order company allows customers to make orders by telephoning operators type the details of the order at a terminal. Explain why the operators use a forms-based interface for taking the details of the orders.	e
		[1]	 3]
	/ii\		_
	(11)	The orders are stored in a random access file. Explain how a new order can b stored using the order ID.	B
			3]
)	On	a regular basis, data is archived.	
	(i)	Explain what is meant by archiving.	
			••
	(ii)	Describe the reasons why it is necessary.	
			 4]
		L. Control of the con	- 1

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	8	
	uilding firm has a main office with stand-alone computers for the workers to use. decided to link these stand-alone computers to make a network. State two items of hardware and one type of software which would be necessary to create the network. Hardware 1	For iner's
(a)	State two items of hardware and one type of software which would be necessary to create the network.	Tage Co.
	Hardware 1	133
		1
	Hardware 2	
	Software	
	[3]	
(b)	When data is passed around a network it can be corrupted. Explain how check sums can be used to detect errors in transmitted data.	
	[4]	

(6)	firm	i:
	(i)	Word processor
		[2]
	(ii)	Spreadsheet
		ro1
		[2]
(d)	Exp	e firm has a warehouse where all materials used in the building work are stored. Dain how a stock control system can be used to monitor the stock in the warehouse. Hould ensure that work does not have to stop because the firm has run out of an in.
		[5]
		[၁]

[5]

9 (a) Complete the table to show the outputs for the possible inputs to this circuit.



Α	В	W	X	С	S
0	0				
0	1				
1	0				
1	1				

(b) State a possible use for this circuit in a processor.

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