

GCE

Electronics

Unit F611: Simple Systems

Advanced Subsidiary GCE

Mark Scheme for June 2018

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This mark scheme is published as an aid to teachers and students, to indicate the requirements of the examination. It shows the basis on which marks were awarded by examiners. It does not indicate the details of the discussions which took place at an examiners' meeting before marking commenced.

All examiners are instructed that alternative correct answers and unexpected approaches in candidates' scripts must be given marks that fairly reflect the relevant knowledge and skills demonstrated.

Mark schemes should be read in conjunction with the published question papers and the report on the examination.

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These are the annotations, (including abbreviations), including those used in scoris, which are used when marking

Annotation		Meaning of annotation
	BP	Blank Page – this annotation must be used on all blank pages within an answer booklet (structured or unstructured) and on each page of an additional object where there is no candidate response.

r				
1	BOD	31	BOD	Benefit of doubt
2	×	21	Cross	Cross
3	ECF	241	ECF	Error carried forward
4	NBOD	191	NBOD	Benefit of doubt not given
5	2	1841	Not Relevant	Expandable vertical wavy line
6	REP	271	REP	Repeat
7	TV	201	ΤV	Too vague
8	~	11	Tick	Tick
9	0	1741	ZERO	Zero (big)
10				

Question	Grade	Expected answer					Additional guidance
1a	E	NAND ga	ate			1	
1b							
			Α	В	С		
			0	0	1		
			0	1	1		
			1	0	1		
			1	1	0		
	E	all combi	inations of .	A and B		1	
1c	С	$C = \overline{A \bullet}$				1	$C = \overline{A} \cdot \overline{B} + \overline{A} \cdot B + A \cdot \overline{B}$
1d	С	Logic gates can only supply a few millamps at output (wtte)					Allow Logic gates can only supply a very small/insufficient current at output
	С	MOSFET can <u>switch</u> large current (with virtually no current at gate) (wtte)					Require some notion of MOSFET operation e.g. Allow MOSFETs <u>amplify/boost</u> current for [1]
1e	E	W = 0.3	x 5 = 1.5 W	1		1	

Question	Grade	Expected answer	Mark	Additional guidance
1f	А	Correct MOSFET symbol	1	
	D	MOSFET and lamp in series with power to allow current	1	
		to flow		
	В	Source to 0 V Drain to lamp to provide 5 V to lamp	1	No marks for this if first two marks not awarded
	D	Gate to output of NAND gate	1	
1g	А	Switch from one input to 0 V	1	
	А	Both inputs correct	1	
	D	Switch and resistor form potential divider across power	1	
		supply		

Question	Grade	Expected answer	Mark	Additional guidance
2a	D	5-2.1=2.9 V (Correct voltage across R)	1	
	E	2.9/330=0.0088 A (voltage divided by 330 Ω)	1	
2b	Е	R at least 10 k Ω	1	
	Е	Use of 0.7 RC	1	
	D	RC = 21.4 s	1	
2c	В	LED stops glowing	1	
	E	For 15s	1	
	D	Then turns on again (ecf from first mark)	1	No mark if answer indicates slow charging

Question	Grade	Expected answer	Mark	Additional guidance
2d	E	U goes high high at start, goes low between 5 s and 10 s	1	
	С	$W = \overline{U \cdot Y}$	1	0 5 70 15 20 .25 time/a
	В	X changes from 0 V to 5 V at 5 s	1	5
	В	X decay exponentially	1	0 5 10 15 20 25 time/s
	А	To 2.5 V at 20 s then goes low suddenly	1	
	Е	Y starts high	1	0 5 10 16 25 tms/s
	В	Goes low between 5 s and 20 s then high after 20 s (ecf	1	
		from X)	1	b s to the set of the
	A	Z same shape as Y but only at 0 V and 2.1 V (by eye)		0 9 9 9 9 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1

Question	Grade	Expected answer	Mark	Additional guidance
3a	E	Vout=13v	1	Evidence of correct output voltage used
	E	Voltage across R is 13-3.6=9.4 V ecf 15-3.6=11.4V	1	Evidence of subtracting 3.6 v from output
	E	I=8mA=0.008A	1	Correct converstion from milli
	E	R=9.4/0.008	1	Correct use of Ohm's law
	E	R=1200Ω	1	Correct answer (1) allow ecf at each stage
				1200 Ω allow more sig figs (1175 Ω) [5]
3b	E	Analogue: any value (between minimum and maximum)	1	Ignore comments about A
	E	Digital: one of only two values	1	
	E	Analogue: voltage at B	1	
	E	Digital: Voltage at C or D	1	
3ci	E	Ring around zener	1	
3cii	D	(sharp, $\Delta V < 1 V$) rise from zero current in +ve quadrant	1	
		(sharp) fall from zero current in –ve quadrant		
	С	departs from 0V axis at 0.7V	1	Max 2 if rises not sharp
	В	departs from 0V axis at -5.6V	1	
3ciii	E	5.6V	1	Do not accept -3.6V
3d	Е	Resistance falls with increasing light intensity	1	
	E	Line curves to be asymptotic to axes	1	Accept log-log plot
3e	С	Total resistance is 10k + 6.8k = 16.8k	1	
	С	Current through LDR is 15/16.8k=0.00089A	1	
	С	Voltage across 6.8k is 0.00089x6.8k=6.1V	1	

Question	Grade	Expected answer	Mark	Additional guidance
3f	А	LED reverse biased	1	
	D	Output is -13V	1	Accept "saturated negative"
	В	because voltage at inverting input (B) > voltage at non-	1	comparison of voltages at op-amp inputs
		inverting input (A)		
3g	С	Voltage at inverting input (B) = 5.6v	1	
	С	Current through 6.8k is 5.6/6.8k=0.823mA ecf	1	
	В	Voltage across LDR is 15- 5.6=9.4v ecf	1	Use of potential divider rule with 6.8k Ω
	Α	Resistance of LDR is 9.4/0.00082=11400 Ω ecf	1	Accept 11.4 k Ω for 4 marks

Question	Grade	Expected answer	Mark	Additional guidance
4a	С	$Q = \overline{A + \overline{B}}$	1	3 rd
4b	А	$P = (\overline{\overline{C} \cdot \overline{D}}) \cdot \overline{D}$	1	3 rd
4c	А	$R = (\overline{E} + F) \cdot (E + \overline{F})$	1	4 th
4d	А	$S = (\overline{G} + H) \cdot (G + \overline{G})$	1	2 nd

Mark Scheme

Question	Grade	Expected answer	Mark	Additional guidance
5ai	E	Correct identification of period (6 divisions)	1	400ms worth 1 mark
	Е	Correct conversion from divisions to ms (x500ms)	1	
		3s		
5aii	E	0.33Hz	1	
5aiii	Е	5 V	1	
5bi	Е	Identifiable oscilloscope symbol connected to 0V	1	
	Е	Connection input of OR	1	
5bii	Е	0.33 s	1	
5biii	В	+ve spikes – Sharp rise, slow fall	1	Shape
	В	+ve elements go up 2.5 squares	1	Amplitude
	А	+ve elements period 6 squares	1	Period
	А	+ve spikes decay half voltage in ~half a division	1	Decay time
	А	-ve spikes <1 square sharp rise then decay before +ve	1	
		spikes half way between +ve spikes		
5biv	С	Lamp flashes on and off repeatedly	1	No credit for gradual change in LED intensity
	А	with period of 3 s	1	Give mark if on period + off period = 3 s
	А	On for about 0.25 s (0.7RC)	1	Allow 0.2 s – 0.3 s

Question	Grade	Expected answer						Additional guidance									
6a	Е	M = k					1										
	D	$N = \overline{J}$	K + K				1										
	D	$P = \overline{\overline{J}}$	+K	P =	J + K		1										
	В	$Q = (\bar{J}$	$\overline{\overline{I+K}}$) \cdot (K +	-L) $Q =$	$(J+K)\cdot(K$	(L + L)	1	(brackets needed)									
6b				I	I	I	,										
			М	Ν	Р	Q											
	E		0	1	0	0	1	1 mark for each correct column									
	E				1	1	0	0		allow ecf from N to P							
	E			-				allow ecf from M and P to Q									
	E		1	0	1	1											
												1	0	1	1		
													0	0	1	0	
						1	0	1	1								
			1	0	1	1											
			1	0	1	1											

Question	Grade	Expected answer	Mark	Additional guidance
6c	E	NOR between J, K and N	1	
	D	OR between L,K and M	1	
	С	NOT between N and P	1	
	А	AND between P, M and Q	1	

Quality of Written Communication

- 3 The candidate expresses complex ideas extremely clearly and fluently. Sentences and paragraphs follow on from one another smoothly and logically. Arguments are consistently relevant and well structured. There will be few, if any, errors of grammar, punctuation and spelling.
- 2 The candidate expresses straightforward ideas clearly, if not always fluently. Sentences and paragraphs may not always be well connected. Arguments may sometimes stray from the point or be weakly presented. There may be some errors of grammar, punctuation and spelling, but not such as to suggest a weakness in these areas.
- 1 The candidate expresses simple ideas clearly, but may be imprecise and awkward in dealing with complex or subtle concepts. Arguments may be of doubtful relevance or obscurely presented. Errors in grammar, punctuation and spelling may be noticeable and intrusive, suggesting weaknesses in these areas.
- 0 The language has no rewardable features.

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