

Cambridge National

Engineering

Unit R113: Electronic principles

Level 1/2 Cambridge National Award/Certificate in Systems Control in Engineering

Mark Scheme for June 2018

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This mark scheme is published as an aid to teachers and students, to indicate the requirements of the examination. It shows the basis on which marks were awarded by examiners. It does not indicate the details of the discussions which took place at an examiners' meeting before marking commenced.

All examiners are instructed that alternative correct answers and unexpected approaches in candidates' scripts must be given marks that fairly reflect the relevant knowledge and skills demonstrated.

Mark schemes should be read in conjunction with the published question papers and the report on the examination.

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Annotations

Annotation	Meaning
BP	Blank page
VG	Vague
	Tick
SEEN	Noted but no credit given
REP	Repeat
K	Knowledge
EG	Example/Reference
DEV	Development
×	Cross
BOD	Benefit of doubt
?	Unclear
L3	Level 3
L2	Level 2
LI	Level 1

	Ques	tion	Answer	Mark	Guidance
1	(a)		AND Gate		Award one mark for each correct graphical symbol.
			Capacitor		
			Fuse		
			Buzzer		
1	(b)		I = V/R = 2/100 = 0.02 A	3	Award one mark for I = V/R. Award one mark for 2/100. Award one mark for 0.02. Award three marks for 0.02 or 0.02 A without any workings.
1	(c)		$P = I^{2}R$ = $2^{2} \times 220$ = 880 W	3	Award one mark for $P = I^2R$. Award one mark for $2^2 \times 220$. Award one mark for 880. Award three marks for 880 or 880 W without any workings.
			Total	10	Accept any alternative correct method of solution
			l	10	

	Ques	stion				Answ	er			Mark	Guidance
2	(a)		Micropl Piezo-e Pressu Seven	Crystal Dis hone electric buz re switch Segment c	play module zer	Output ✓ ✓	device			4	Award zero marks if more than four boxes are ticked If four boxes are ticked award one mark for each correct response.
2	(b)		NCT thermistor $f = 1/ (1.38 \text{ RC}).$ $R = 110 \text{ k}\Omega = 110 \text{ x } 10^3 \Omega$ $C = 3.3 \text{ \muF} = 3.3 \text{ x } 10^{-6} \text{ F}$ $f = 1/(1.38 \text{ x } 110 \text{ x } 10^3 \text{ x } 3.3 \text{ x } 10^{-6})$ $= 2 \text{ Hz}$					3	Award one mark for $R = 110 \text{ k}\Omega = 110 \text{ x } 10^3 \Omega$. Award one mark for $C = 3.3 \mu\text{F} = 3.3 \text{ x } 10^{-6} \text{F}$. Award one mark for $f = 1/(1.38 \text{ x } 110 \text{ x } 10^3 \text{ x } 3.3 \text{ x } 10^{-6})$ = 2 or 2 Hz. Award three marks for 2 or 2 Hz without any workings.		
2	(c)			0 0 1	1 0 1	OR gate output 0 1 1	AND output 0		NAND gate output 1 1 0	3	Award one mark for each correct column.
							•	•	To	otal 10	

Qı	estio	n		Answer	Mark	Guidance
3	(a)	(i)	circuit boards. (1) OR The circuit is passed over a p A pump produces an upwellir	oulk soldering process (1) used in the manufacture of printed	2	Award one mark for each correct point made up to a maximum of two marks
3	(a)	(ii)	to soldering. (1) OR Pick-and-Place machines use In general, pick-and-place m through-hole insertion machin Some of these machines cut	e vacuum pickup tools to hold the components. achines offer better speed, accuracy, and flexibility than nes. and form the leads of the IC package at the time of omponent lead damage due to mishandling.	2	Award one mark for each correct point made up to a maximum of two marks
3	(b)		Sequence of events 1 2 3 4 5 6 7	Apply flux to all pads on the circuit board. The resistor should now be fastened on one side; apply solder to the soldering tip again and touch the iron tip on the other side. Check that the tip of the soldering iron and the resistor are clean. Inspect the solder joints with a magnifying glass to make sure the connection is good. Place the resistor in position and hold it there with a pair of tweezers. Touch the soldering tip so that it heats both the resistor and circuit board pad. Apply some solder to the tip of the iron and touch the circuit board pad with the tip so that some of the solder passes on to the pad.	6	Award one mark for each correct line 2/3/4/5/6/7. Event 1 is given in the question.
				Total	10	

Question	<u>Marks</u>	Mark	Answer
4 (a)*	 Detailed discussion showing a thorough understanding of the function and applications of an LDR and an LED in electronic circuits. Information is presented clearly and accurately, with correct use of appropriate technical language and engineering terminology. Accurate use of spelling, punctuation and grammar. Level 2 (3–4 marks) Adequate discussion showing some understanding of the function and applications of an LDR and an LED in electronic circuits. Information is presented clearly and with some accuracy. Appropriate technical language and engineering terminology is used on some occasions. Occasional errors in spelling, punctuation and grammar. Level 1 (1–2 mark) Basic discussion showing limited understanding of the function and applications of an LDR and an LED in electronic circuits. Information presented is basic and may be ambiguous or badly presented. There will be little or no use of technical language and engineering terminology. Errors of spelling, punctuation and grammar may be intrusive. Level 0 (0 marks) A response that is irrelevant and/or not worthy of a mark. Annotate with 'Seen' at end of response. 	6	When light falls on an LDR the resistance is low. (1) When an LDR is kept in the dark, its resistance is high.(1) Application (1) Light sensor, Burglar alarm circuits Alarm clock Light intensity meters, Street light control Automatic Headlight Dimmer Night Light Control Absence / Presence (beam breaker) Position Sensor Camera Exposure Control Auto Slide Focus - dual cell Photocopy Machines – toner density LED – Output device A light-emitting diode is a semiconductor device that emits visible light (1) when an electric current passes through it.(1) Application (1) Digital clocks, video recorders and microwave ovens. Indicator lights: bar-graph, or alphabetic-numeric readouts. LCD panel backlighting: Specialized white LEDs are used in flat-panel computer displays. Fibre optic data transmission Remote control Keyboard for Num lock, Caps lock and Scroll Lock

	Questic	Answer	Mark	Guidance
4	(b)	$+5V$ 3.3 k Ω output	4	Award one mark for correct LDR symbol (with or without envelope) and position. Award one mark for correct resistor symbol and position. Award one mark for correct 0 – 5V input supply position. Award one mark for correct output voltage position.
		Tota	10	

Qu	estior	n	Answer	Mark	
5	(a)		A V V V Switch 9V	3	Award one mark for correct symbol and position of ammeter. Award one mark for correct symbol and position of voltmeter across one lamp. Award one mark for correct symbol and position of voltmeter across the switch and the cell to measure e.m.f. OR award one mark for correct symbol and position of voltmeter across the cell to measure e.m.f.
5	(b)	(i)	Single Pole Single Throw (SPST)	1	Correct answer only.
5	(b)	(ii)	Signal	1	Correct answer only.
5	(c)	(i)	R = 1.8 + 1.8 + 1.8 = 5.4 Ω	2	Award one mark for R = 1.8 + 1.8 + 1.8. Award one mark for 5.4 or 5.4 Ω . Award two marks for 5.4 or 5.4 Ω without any workings.
5	(c)	(ii)	W = Pt = 10 x 4 = 40 Wh = 0.04 kWh OR W = Pt = 10 x 4 x 60 x 60 = 144000 Ws or 144000 J	3	Award one mark for W = Pt. Award one mark for workings. Award one mark for correct answer with correct unit. Award three marks for correct answer with correct unit with or without workings.
			Total	10	

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Que	stion		Answer	Mark	Guidance
6	(a)		Visual inspection (includes checking) of equipment Portable appliance testing (PAT) compliance Use of residual current device (RCD)	3	Award one mark for each correct response.
6	(b)		Tests can determine whether or not the circuit is fit for purpose. Circuit can be modified and retested quicker than a real circuit. Helps circuit designers test ideas before actually building real circuits, saving much time and money. Relatively simple to use. Simulation can be saved	2	Accept any two correct reasons. Accept other correct reasons.
6	(c)	(i)	A Square wave (accept astable) B Sine wave (accept alternating) C Triangular wave (accept saw tooth)	3	Award one mark for each correct response.
6	(c)	(ii)	D To control amplitude E Frequency multiplier or wavelength multiplier	2	Award one mark for each correct response. Accept alternative correct answers.
			Total	10	

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