



## **Cambridge Technicals**

### **Engineering**

Unit 4: Principles of electrical and electronic engineering

Level 3 Cambridge Technical Certificate/Diploma in Engineering

**05822 - 05825**

### **Mark Scheme for January 2018**

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This mark scheme is published as an aid to teachers and students, to indicate the requirements of the examination. It shows the basis on which marks were awarded by examiners. It does not indicate the details of the discussions which took place at an examiners' meeting before marking commenced.

All examiners are instructed that alternative correct answers and unexpected approaches in candidates' scripts must be given marks that fairly reflect the relevant knowledge and skills demonstrated.

Mark schemes should be read in conjunction with the published question papers and the report on the examination.

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**Annotations**

| <b><i>Annotation</i></b> | <b><i>Meaning</i></b> |
|--------------------------|-----------------------|
| tick                     | Correct response      |
| cross                    | Incorrect response    |
| Omission mark (carat)    | Incomplete response   |
| ECF                      | Error carried forward |
| BOD                      | Benefit of doubt      |
| NBOD                     | No benefit of doubt   |
| RE                       | Rounding error        |

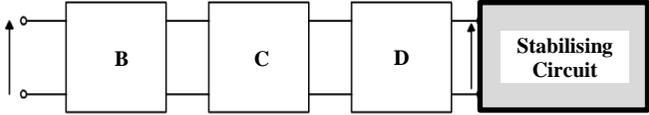
**Subject-specific marking instructions**

In all numerical calculation questions a correct response will gain all marks unless specified otherwise.

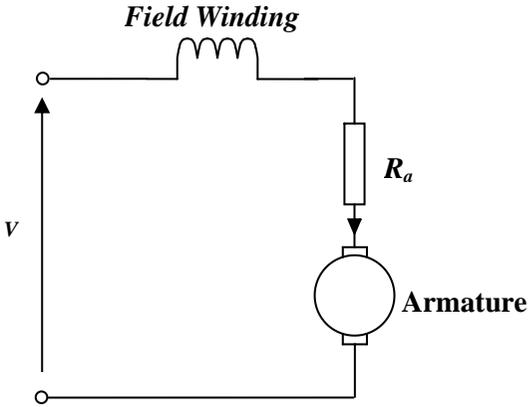
| Question |     | Answer  | Marks       | Guidance   |
|----------|-----|---|-------------|--|
| 1        | (a) | <ul style="list-style-type: none"> <li>• High/infinite gain voltage amplifier</li> <li>• Differential input</li> <li>• Amplifies the difference between the non-inverting and inverting inputs.</li> <li>• Produces an output voltage many times larger than the voltage difference between its inputs.</li> <li>• Has a single ended output.</li> <li>• Has a dual supply which allows the output voltage to swing above and below 0V.</li> <li>• Can be used to amplify both A.C. and D.C.</li> <li>• DC-coupled voltage amplifier</li> <li>• High/infinite input impedance</li> <li>• Low/zero output impedance</li> </ul> | 3           | Any 3 points<br>Do not award a simple description of the diagram, e.g. "has two inputs and one output"   |
|          | (b) | <p>(i)</p> $\text{Voltage Gain } (A_v) = 1 + \frac{R_2}{R_1}$ $20 = 1 + \frac{R_2}{10\text{K}\Omega}$ $(20-1) \times 10000 = R_2$ $R_2 = 190 \text{ k}\Omega$   | 1<br>1<br>1 | 1 mark for substitution<br>1 mark for rearrangement - For applying knowledge from Unit 1 LO1   |
|          |     | <p>(ii)</p> $\text{Voltage Gain } (A_v) = \frac{V_{out}}{V_{in}}$ $V_{out} = \text{Voltage Gain} \times V_{in} = 20 \times 10\text{mV}$ $= 0.2\text{V or } 200\text{mV}$  | 1 +1<br>1   | <b>Units required in answer</b><br>Recalling formula (see or implied) & rearrangement and substitution.<br>Correct unit must be stated or maximum 2 marks. |

| Question |     | Answer  | Marks    | Guidance  |
|----------|-----|---|----------|---|
| 2        | (a) | $X_C = \frac{1}{2\pi f c}$ $X_C = \frac{1}{2 \times \pi \times 50 \times 3.3\mu}$ $X_C = 965 \Omega$  | 1<br>1   | Allow 960 $\Omega$ (rounded to 2 sig. figs.)  |
|          | (b) | (i) $Z = \sqrt{R^2 + X_C^2}$ $Z = \sqrt{964.575 \dots^2 + 200^2} *$ $Z = 985 \text{ or } 986 \underline{\Omega}$ <p>*If rounded answer from (i) 965 used = 985.51 so accept rounding to 986</p> | 1<br>1+1 | <b>Units required in answer</b><br>Use of formula seen or implied. Allow ecf from (i) for $X_C^2$<br>1 mark for correct answer, 1 mark for unit.<br>Allow 980 $\Omega$ if $X_C = 960 \Omega$ in 2(a). |
|          |     | (ii) $\cos \phi = R/Z$ $\cos \phi = 200/985 \text{ or } 200/986 \text{ or } 200/980$ $\text{Phase angle } (\phi) = 78.3^\circ / 78.2^\circ \text{ or } 1.4 \underline{\text{rad}}$              | 1<br>1+1 | <b>Units required in answer</b><br>Allow ecf from (i) for Z<br>1 mark for correct answer, 1 mark for unit.  |

| Question |         | Answer  | Marks       | Guidance  |
|----------|---------|---|-------------|---|
|          | (iii)   |   | 3<br>1      | 1 mark each for the correct position of $V_{Supply}$ , $V_C$ and $\phi$<br>1 mark for correct alignment (see dotted lines).   |
| 3        | (a)     | At any node/junction in a circuit the sum of the currents flowing into the node are equal to the sum of the currents flowing out.   | 2           | 1 mark for recognising it is the current law.<br>2 marks for correct definition.  |
|          | (b) (i) | $I=V/R$<br>$R= 10k\Omega + 2.2k\Omega = 12.2k\Omega$<br>$I= 6/12.2k\Omega$<br>$I= 0.492 \text{ mA}$   | 1<br>1<br>1 | For applying knowledge from Unit 2 LO3  |
|          | (ii)    | $I_1= 6V/33 \text{ k}\Omega = .000182 \text{ (A)} \text{ } 0.182\text{m(A)} \text{ or } 182\mu\text{(A)}$<br>$I=I_1 + I_2 + I_3 \text{ (Kirchhoff's current law)}$<br>$I=0.182\text{mA} + 1\text{mA} +0.492\text{mA}$<br>$I= 1.67\text{mA}$ | 1<br>1<br>2 | <b>Units required in answer</b><br>For applying knowledge from Unit 2 LO3<br><br>Allow ecf for 0.492mA for $I_3$ from (i)<br>Allow ecf for 0.182mA for $I_1$<br>1 mark for correct answer. And 1 mark for correct unit.<br><br>1 mark for unit - For applying knowledge from Unit 2 LO1 |

| Question |          |          | Answer  | Marks            | Guidance  |          |   |   |          |   |   |          |   |   |          |   |   |          |   |                             |
|----------|----------|----------|---|------------------|---|----------|---|---|----------|---|---|----------|---|---|----------|---|---|----------|---|-----------------------------|
| 4        | (a)      | (i)      | B Transformer<br>C Rectifier<br>D Smoothing Circuit<br>E (unregulated/unstabilised) DC output   | 1<br>1<br>1<br>1 | Part marks for BCD may be earned where elements are omitted provided those shown are in a logical sequence.<br>Correct answer only. |          |   |   |          |   |   |          |   |   |          |   |   |          |   |                             |
|          |          | (ii)     | Stabilising circuit connected to DC output.<br>   | 1                | Also accept block labelled <b>voltage regulator</b>   |          |   |   |          |   |   |          |   |   |          |   |   |          |   |                             |
|          | (b)      |          | <ul style="list-style-type: none"> <li>Maintains a constant voltage (or current) on the output</li> <li>Regardless of changes in circuit load</li> </ul>  | 1<br>1           | Up to two marks for an explanation.   |          |   |   |          |   |   |          |   |   |          |   |   |          |   |                             |
| 5        | (a)      | (i)      | <table border="1" data-bbox="584 836 875 1331"> <thead> <tr> <th><i>A</i></th> <th><i>B</i></th> <th><i>Q</i></th> </tr> </thead> <tbody> <tr> <td>0</td> <td>0</td> <td><b>0</b></td> </tr> <tr> <td>0</td> <td>1</td> <td><b>1</b></td> </tr> <tr> <td>1</td> <td>0</td> <td><b>1</b></td> </tr> <tr> <td>1</td> <td>1</td> <td><b>0</b></td> </tr> </tbody> </table> | <i>A</i>         | <i>B</i>  | <i>Q</i> | 0 | 0 | <b>0</b> | 0 | 1 | <b>1</b> | 1 | 0 | <b>1</b> | 1 | 1 | <b>0</b> | 2 | 1 mark for 2/3 rows correct |
| <i>A</i> | <i>B</i> | <i>Q</i> |   |                  |   |          |   |   |          |   |   |          |   |   |          |   |   |          |   |                             |
| 0        | 0        | <b>0</b> |   |                  |   |          |   |   |          |   |   |          |   |   |          |   |   |          |   |                             |
| 0        | 1        | <b>1</b> |   |                  |   |          |   |   |          |   |   |          |   |   |          |   |   |          |   |                             |
| 1        | 0        | <b>1</b> |   |                  |   |          |   |   |          |   |   |          |   |   |          |   |   |          |   |                             |
| 1        | 1        | <b>0</b> |   |                  |   |          |   |   |          |   |   |          |   |   |          |   |   |          |   |                             |



| Question |     | Answer   | Marks       | Guidance   |
|----------|-----|--|-------------|--|
| 6        | (a) |  <p>The diagram shows a series circuit. On the left, there is a voltage source labeled 'V' with an upward-pointing arrow. The circuit continues to the right through a component labeled 'Field Winding' represented by a coil symbol. Below the field winding is a resistor labeled 'Ra'. To the right of the resistor is a circular component labeled 'Armature' with two terminals at the bottom. The circuit then returns to the bottom terminal of the voltage source 'V'.</p> | 4           | 1 mark each for correct symbols for armature, field winding, output voltage and armature resistance connected in series.                                 |
|          | (b) | (i) <ul style="list-style-type: none"> <li>• <math>V = I_a R_L</math></li> <li>• <math>V = 68 \times 7</math></li> <li>• <math>V = 476 \text{ V}</math></li> </ul>   | 1<br>1      | For applying knowledge from Unit 2 LO3<br>Unit <b>not</b> required for this mark.  |
|          |     | (ii) <ul style="list-style-type: none"> <li>• <math>V = E - I_a R_a</math></li> <li>• <math>476 = E - (1.5 \times 7)</math></li> <li>• <math>E = 486.5 \text{ V}</math></li> </ul>   | 1<br>1<br>1 | For substitution<br>For rearrangement- For applying knowledge from Unit 1 LO1<br>Allow ecf for 476 V from (i)<br>Unit <b>not</b> required for this mark. |
|          | (c) | <ul style="list-style-type: none"> <li>• Advantage: Lower internal resistance.</li> <li>• Disadvantage: Requires additional voltage supply for field winding circuit.</li> </ul>   | 1<br>1      |  |

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